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PTO/SB/08A (08-00)

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		<b>Complete if Known</b>	
Application Number		10/743,906	
Filing Date		12/24/2003	
First Named Inventor		Ryu OGAWARA et al.	
Group Art Unit		2824	
Examiner Name		Unknown	
Attorney Docket Number		001701.00198	
Sheet	1	of	1

## U.S. PATENT DOCUMENTS

Examiner Initials *	'Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
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## FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
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#### OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

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Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
at		D. Takashima, et al., "Gain Cell Block Architecture for Gigabit-Scale Chain Ferroelectric RAM," 1999 Symposium on VLSI Circuits, pp. 103 - 104		
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Examiner Signature		Date Considered	01/13/2006
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